

<IGBT Modules>

# CM600DX-13T/CM600DXP-13T

**HIGH POWER SWITCHING USE INSULATED TYPE** 

		Collector current Ic	600A	
	H CO	Collector-emitter voltage VCES	650V	
		Maximum junction temperature $T_{vjmax}$	<b>1 7 5</b> °C	
DX		●Flat base type		
		<ul> <li>Copper base plate (Nickel-plating)</li> </ul>		
		RoHS Directive compliant		
		<ul> <li>Tin-plating pin terminals</li> </ul>		
		Collector current Ic	600A	
		Collector-emitter voltage V <sub>CES</sub>	650V	
		Maximum junction temperature $T_{vjmax}$	<b>1 7 5</b> °C	
DXP	P AL	●Flat base type		
		<ul> <li>Copper base plate (Nickel-plating)</li> </ul>		
	and a state	<ul> <li>RoHS Directive compliant</li> </ul>		
		<ul> <li>Tin-plating pressfit terminals</li> </ul>		
	dual switch (half-bridge)	•UL Recognized under UL1557, File No. E32358	35	

## APPLICATION

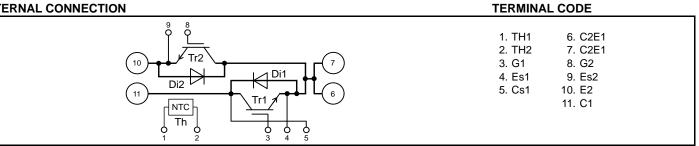
AC Motor Control, Motion/Servo Control, Power supply, etc.

## **OPTION (Below options are available.)**

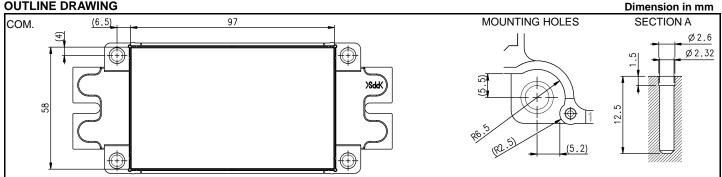
•PC-TIM (Phase Change Thermal Interface Material) pre-apply

### •V<sub>CEsat</sub> selection for parallel connection

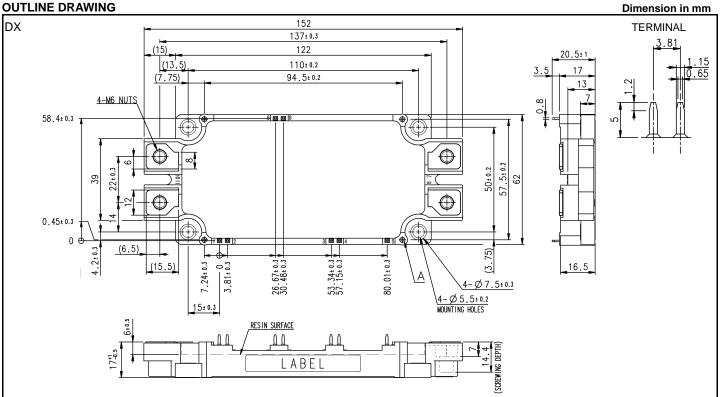
## INTERNAL CONNECTION



### **OUTLINE DRAWING**



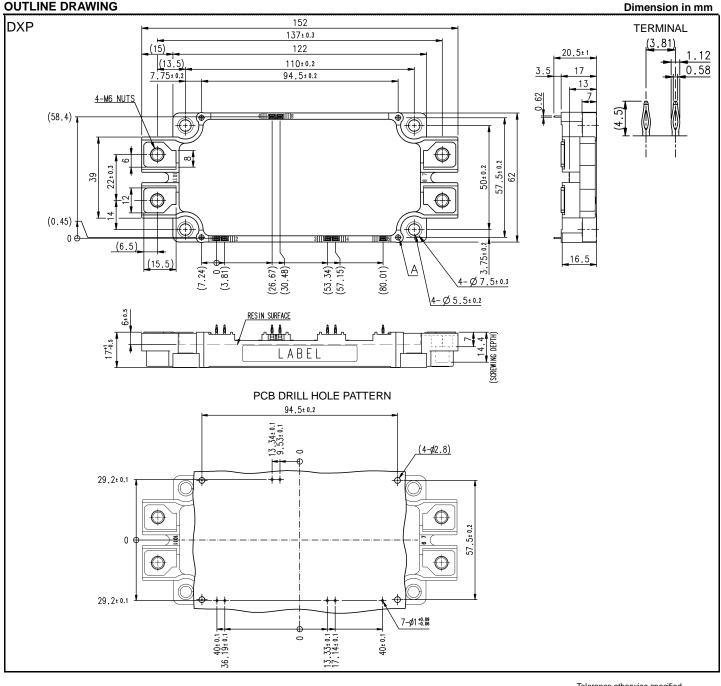




#### Tolerance otherwise specified

Division of	Tolerance	
0.5 to 3		±0.2
over 3	to 6	±0.3
over 6	to 30	±0.5
over 30	to 120	±0.8
over 120	to 400	±1.2

## **OUTLINE DRAWING**



Iolerance otherwise specified							
Divisio	on of	Tolerance					
	0.5	to	3	±0.2			
over	3	to	6	±0.3			
over	6	to	30	±0.5			
over	30	to	120	±0.8			
over	120	to 4	400	±1.2			

## MAXIMUM RATINGS (Tvj=25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	650	V
$V_{\text{GES}}$	Gate-emitter voltage	C-E short-circuited	± 20	V
I <sub>C</sub>	Collector current	DC, T <sub>C</sub> =101 °C (Note2, 4)	600	
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	1200	A
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	2080	W
IE (Note1)		DC (Note2)	600	
IERM (Note1)	Emitter current	Pulse, Repetitive (Note3)	1200	A

#### MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
$T_{vjmax}$	Maximum junction temperature	Instantaneous event (overload)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note4)	125	C
T <sub>vjop</sub>	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T <sub>stg</sub>	Storage temperature	-	-40 ~ +125	

## ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

Symbol Item		Conditions			Limits		Unit
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
ICES	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	Ic=60 mA, Vce=10 V		5.4	6.0	6.6	V
V <sub>CEsat</sub>		I <sub>C</sub> =600 A, V <sub>GE</sub> =15 V,	T <sub>vj</sub> =25 °C	-	1.45	1.80	
		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.55	-	V
(Terminal)		(Note5)	T <sub>vj</sub> =150 °C	-	1.60	-	1
	Collector-emitter saturation voltage	Ic=600 A,	T <sub>vj</sub> =25 °C	-	1.30	1.55	
V <sub>CEsat</sub>		V <sub>GE</sub> =15 V,	T <sub>vj</sub> =125 °C	-	1.35	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.35	-	1
Cies	Input capacitance			-	-	80.2	
C <sub>oes</sub>	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	3.4	nF
Cres	Reverse transfer capacitance	-		-	-	1.5	
$Q_{G}$	Gate charge	V <sub>CC</sub> =300 V, I <sub>C</sub> =600 A, V <sub>GE</sub> =15 V	V <sub>CC</sub> =300 V, I <sub>C</sub> =600 A, V <sub>GE</sub> =15 V		2.48	-	μC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>cc</sub> =300 V, I <sub>c</sub> =600 A, V <sub>GE</sub> =±15 V,		-	-	400	
tr	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time			-	-	400	ns
t <sub>f</sub>	Fall time	$-R_{G}=1.0 \Omega$ , Inductive load		-	-	400	
		I <sub>E</sub> =600 A, G-E short-circuited,	T <sub>vj</sub> =25 °C	-	1.60	2.15	
V <sub>EC</sub> (Note1)		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.65	-	V
(Terminal)		(Note5)	T <sub>vj</sub> =150 °C	-	1.65	-	1
	Emitter-collector voltage	I <sub>E</sub> =600 A,	T <sub>vj</sub> =25 °C	-	1.45	1.85	
V <sub>EC</sub> <sup>(Note1)</sup>		G-E short-circuited,	T <sub>vj</sub> =125 °C	-	1.50	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.50	-	1
t <sub>rr</sub> <sup>(Note1)</sup>	Reverse recovery time	V <sub>CC</sub> =300 V, I <sub>E</sub> =600 A, V <sub>GE</sub> =±15 V,	•	-	-	500	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge	R <sub>g</sub> =1.0 Ω, Inductive load		-	42	-	μC
Eon	Turn-on switching energy per pulse	$V_{cc}$ =300 V, $I_c$ = $I_E$ =600 A, $V_{GE}$ =±15 V, $R_G$ =1.0 Ω, $T_{vj}$ =150 °C,		-	8.3	-	
E <sub>off</sub>	Turn-off switching energy per pulse			-	33.2	-	mJ
Err (Note1)	Reverse recovery energy per pulse	Inductive load		-	23.9	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, T <sub>C</sub> =25	5 °C (Note4)	-	0.71	-	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	1.0	-	Ω

# ELECTRICAL CHARACTERISTICS (cont.; $T_{vj}$ =25 °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
	nem	Conditions	Min.	Тур.	Max.	Unit
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	$R_{100}$ =493 Ω, $T_C$ =100 °C (Note4)	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note6)	-	3375	-	K
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note4)	-	-	10	mW

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit	
Symbol	nem			Min.	Тур.	Max.	Unit	
$R_{th(j-c)Q}$	Junction to case,		er Inverter IGBT (Note4)	-	-	72	K/kW	
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)		-	-	111	r\/KVV	
D	Contact thermal resistance	Case to heat sink,	Thermal grease applied (Note4, 7)	-	11.5	-	K/kW	
R <sub>th(c-s)</sub>	Contact thermal resistance	per 1 module,	PC-TIM applied (Note4, 8)	-	3.1	-	r/kvv	

#### MECHANICAL CHARACTERISTICS

Sumbol	ltom	Conditions			Unit		
Symbol	Item	Con	Conditions		Тур.	Max.	Unit
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
		Calder sin type (DV)	Terminal to terminal	17	-	-	
	Creepage distance	Solder pin type (DX)	Terminal to base plate	16.4	-	-	mm
ds		Pressfit pin type (DXP)	Terminal to terminal	17	-	-	
			Terminal to base plate	16.8	-	-	mm
		Solder pin type (DX)	Terminal to terminal	10	-	-	mm
			Terminal to base plate	16.2	-	-	
da	Clearance	Dressfit sig type (DVD)	Terminal to terminal	10	-	-	
		Pressfit pin type (DXP) Terminal to base plate		16.2	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note9)		±0	-	+200	μm
m	mass	-		-	300	-	g

\*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

2. Junction temperature (T  $_{\nu j}$  ) should not increase beyond T  $_{\nu j\,m\,a\,x}$  rating.

3. Pulse width and repetition rate should be such that the device junction temperature (T  $_{vj}$ ) dose not exceed T  $_{vjmax}$  rating.

4. Case temperature (T<sub>c</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

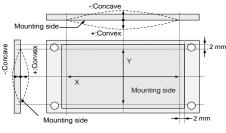
6. 
$$B_{(25/50)} = \ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 $R_{25}\!\!:$  resistance at absolute temperature  $T_{25}$  [K];  $T_{25}\!\!=\!\!25$  [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50} {=} 50$  [°C]+273.15=323.15 [K]

7. Typical value is measured by using thermally conductive grease of  $\lambda=0.9$  W/(m·K)/D<sub>(C-S)</sub>=50 µm.

- 8. Typical value is measured by using PC-TIM of  $\lambda{=}3.4$  W/(m·K)/D<sub>(C-S)</sub>=50  $\mu m.$
- 9. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.

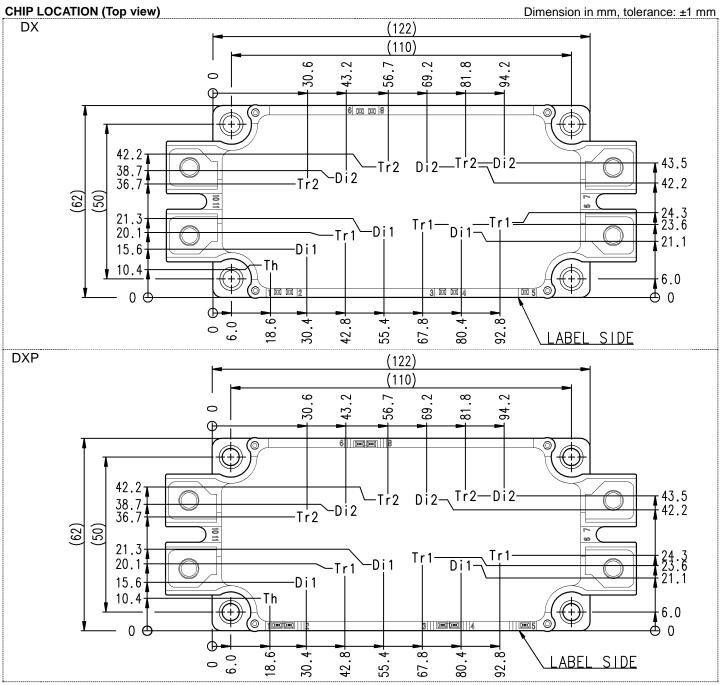


Note10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs. PCB thickness : t=1.6.

	Туре	Manufacturer	Size	Tightening torque (N•m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N∙m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N∙m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N∙m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N⋅m	
	tapping screw		φ2.6×12	0.75 ± 0.075 N-III	

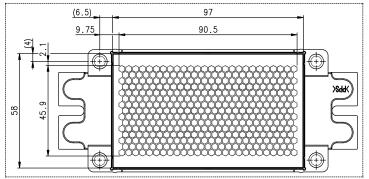
### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Item	Conditions	Limits			Unit
	nem	Conditions		Тур.	Max.	Unit
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals		300	450	V
$V_{\text{GEon}}$	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
R <sub>G</sub>	External gate resistance	Per switch	1.0	-	10	Ω

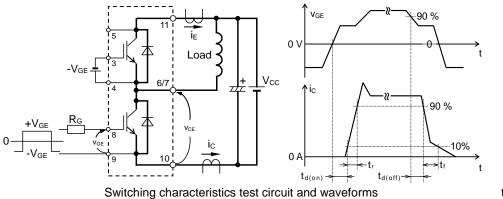


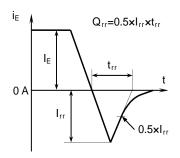
Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor





## TEST CIRCUIT AND WAVEFORMS



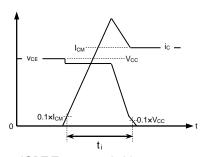


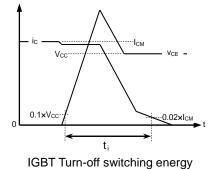
trr, Qrr characteristics test waveform

VEC

Vcc

IEM





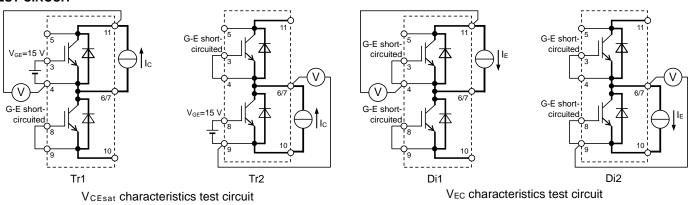
FWD Reverse recovery energy

0 A

IGBT Turn-on switching energy

Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

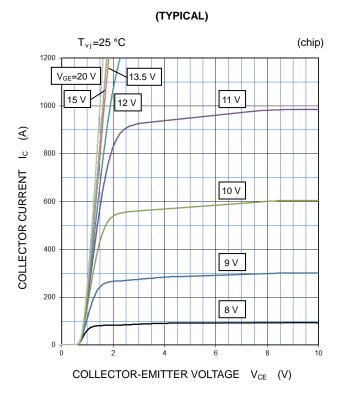
#### **TEST CIRCUIT**



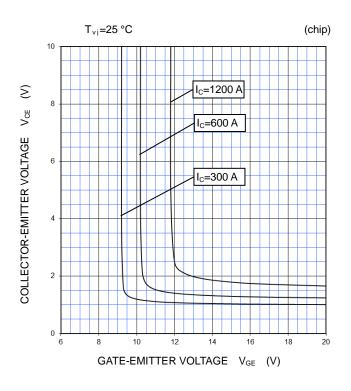
## PERFORMANCE CURVES

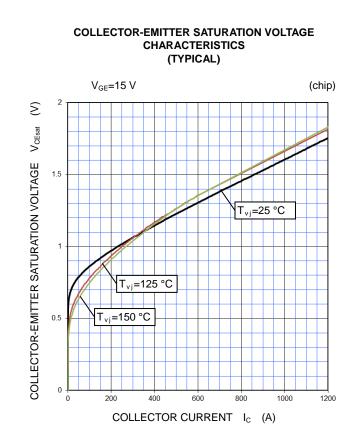
#### **INVERTER PART**

**OUTPUT CHARACTERISTICS** 

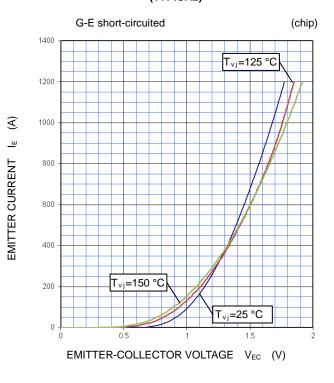


## COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)





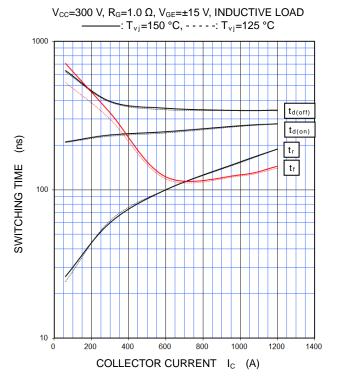
#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



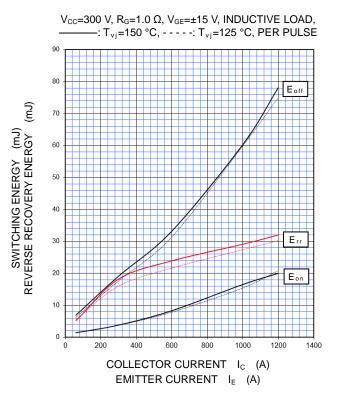
## PERFORMANCE CURVES

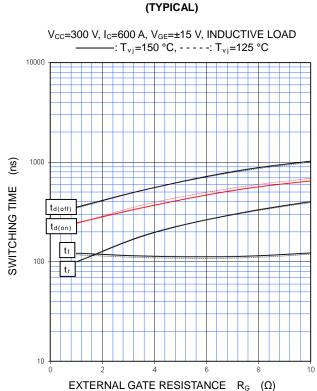
#### **INVERTER PART**

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

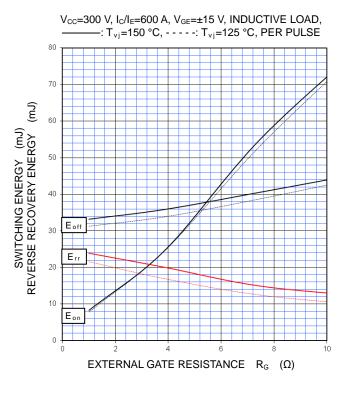


#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)





#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

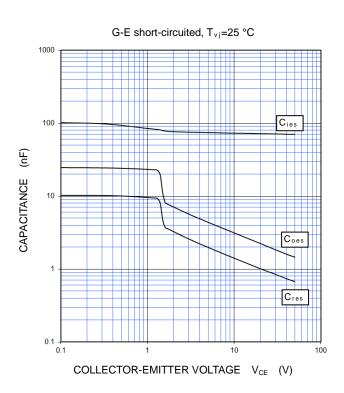


HALF-BRIDGE SWITCHING CHARACTERISTICS

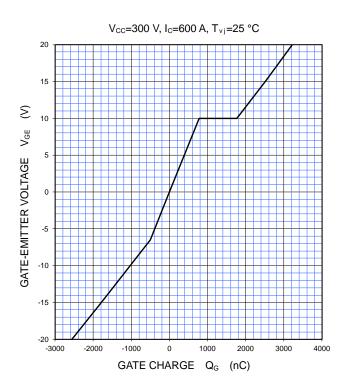
### PERFORMANCE CURVES

#### **INVERTER PART**

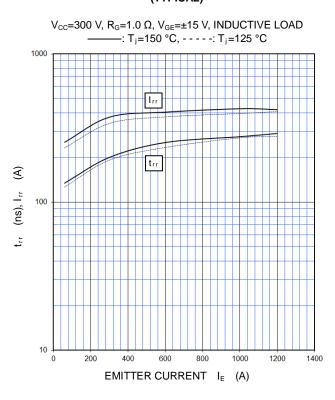
CAPACITANCE CHARACTERISTICS (TYPICAL)



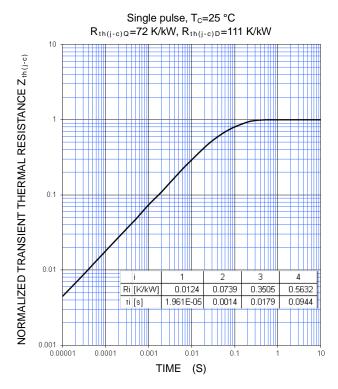
#### GATE CHARGE CHARACTERISTICS (TYPICAL)



#### FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



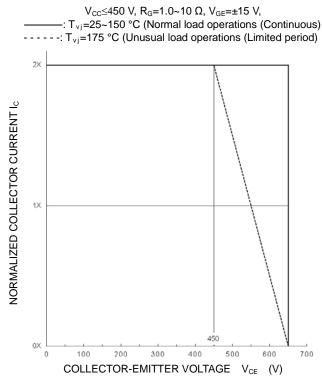
#### TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



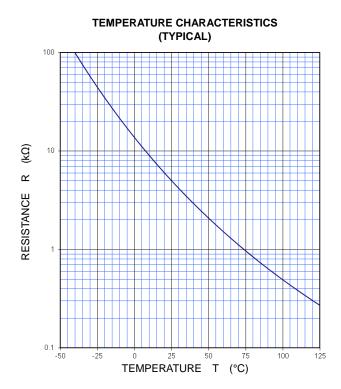
## PERFORMANCE CURVES

#### **INVERTER PART**

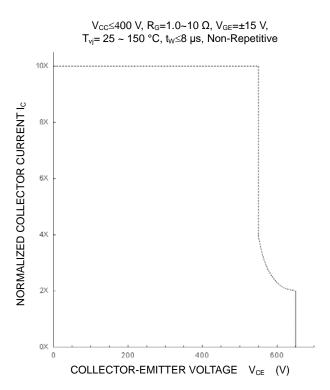
#### TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



NTC thermistor part



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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